# DATA IMAGE CORPORATION

# **LCD Module Specification**

## ITEM NO.: TG241610FFEBB-01

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с	2003/4/25		23

# 2. RECORD OF REVISION

Rev	Date	Item	Page	Comment
В	3/APR/03	15	21	ADD attach 3M tape on FPC.
C	25/APR/03			Change the PCB trace width that closed to the PIN to improve the HOT bar Process quality and update the PCB version from F to G.

# 3. GENERAL SPECIFICATION

Display Format :	240 (W) $\times$ 160 (H) dots
Dot Size :	0.23 (W) × 0.23 (H) mm
View Area :	60 (W) × 41 (H) mm
Outline Dimensions :	70.5 (W) $\times$ 50.3 (H) $\times$ 6.3 (T) mm Max.
Weight :	g max.
LCD Type :	STN Gray STN BLUE VFSTN TN
Polarizer mode :	Reflective V Transflective
	Transmissive Negative Paper white
View Angle :	V 6 O'clock 12 O'clock Others
Backlight :	LED VEL CCFL
Backlight Color :	Yellow green Amber V Blue Green
	White Others
Controller / Driver :	COM NT7701,SEG LH1562
Temperature Range :	NormalV Wide TemperatureOperating 0 to 50°COperating -20 to 70°CStorage-20 to 70°CStorage-30 to 80°C-30 to 80°C

### 4. ABSOLUTE MAXIMUM RATINGS

#### 4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

				$v_{55} = 0v, 1$
Item	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	Vdd-Vss	0	7.0	V
Supply Voltage (LCD Driver)	VLCD	0	30	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Tstg	-30	80	°C

#### 4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

Item	Operating		Sto	rage	Comment	
nem	(Min.)	Max.)	(Min.)	(Max.)	Comment	
Ambient Temp	-20	70	-30 80		Note (1)	
Humidity	Note (2)		Note(2)		Without Condensation	
Vibration		4.9M/S <sup>2</sup>		19.6M/S <sup>2</sup>	XYZ Direction	
Shock		29.4M/S <sup>2</sup>		490M/S <sup>2</sup>	XYZ Direction	

Note(1) Ta =  $0^{\circ}C$  : 50Hr Max.

Note(2) Ta  $\leq 40^{\circ}$ C : 90% RH Max.

Ta  $\geq 40^{\circ}$ C : Absolute humidity must be lower than the humidity of 90% RH at 40°C.

# 5. ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition		Min.	Тур.	Max.	Unit	
Supply Voltage (Logic)	VDD-VSS			2.7	3.0	3.3	V	
		-20°C		18.5	19	19.5		
Supply Voltage	14.05	-10°C		17.6	18.1	18.6	V	
(LCD)	VLCD	25°C		16.5	17.5	18.0		
		70°C		15.7	16.2	16.7		
Innut Valtaga	VIH			0.8*VDD		Vdd	V	
Input Voltage	VIL			Vss		0.2*Vdd	V	
		LCD- ON- EL	ON		26.3		mA	
Logic Supply Current	IDD	LOD- ON- EL	OFF		4.0		mA	
		LCD- OFF-EL	OFF		0.15		mA	

# 6. ELECTRO-OPTICAL CHARACTERISTICS

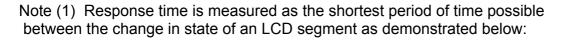
ITEM	Symbol	Condition	Min.	Тур.	Max.	Unit	Ref.	
Diao Timo	Tr	0°C						
Rise Time	Tr	25°C		180	360	ms	Note (1)	
Fall Time	Tf	0°C				me		
	11	25°C		260	520	ms		
Contrast	CR	25°C	2	4			Note (3)	
View Angle	θ1~θ2	25°C &	60				Note (2)	
View Angle	Ø1, Ø <b>2</b>	CR≥3	90				Note (2)	
Frame Frequency	Ff	25°C		70		Hz		

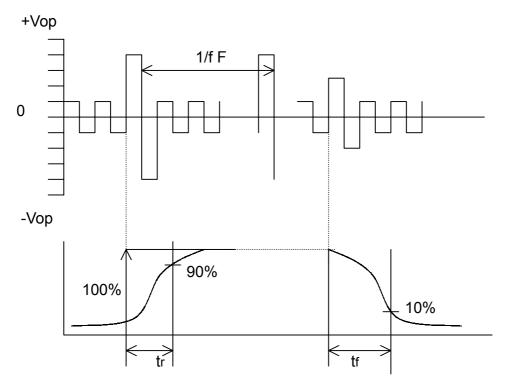
Note (1) & (2) : See next page

Note (3): Contrast ration is defined under the following condition:

CR= Brightness of non-selected condition Brightness of selected condition

- ( a ). Temperature ----- 25°C
- (b). Frame frequency ---- 70Hz
- (c). Viewing angle -----  $\theta = 0^{\circ}$ ,  $\emptyset = 0^{\circ}$
- (d). Operating voltage --- 17.5





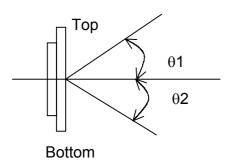
Condition:

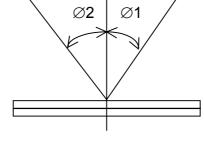
- (a). Temperature -----25°C
- (b). Frame frequency ----- 70Hz
- ( c ) . View Angle ------  $\theta$  = 0°, Ø=0°
- (d). Operating voltage -----17.5

Note (2) Definition of View Angle

Top – Bottom direction

Right -- Left direction





Left Right

### 6.1 ELECTRIC - OPTICAL CHARACTERISTIC

<u>(TEMP:20°</u>C)

ITEM	CONDITION	DIMENION		STANDARD			
	CONDITION			MIN	TYP	MAX	
BRIGHTNESS	400H <sub>z</sub> ,100Vrms SINE WAVE	cd/m <sup>2</sup>		45	56		
UNIFORMITY	400H <sub>z</sub> ,100Vrms SINE WAVE	%		85	90		
CURRENT CONSUMPTION	400H <sub>z</sub> ,100Vrms SINE WAVE	m <sub>A</sub> /cm <sup>2</sup>			0.12	0.17	
	400H <sub>z</sub> .100Vrms	BLUE	Х	0.15	0.17	0.19	
CHROMATICITY		GREEN	Y	0.33	0.35	0.37	
CHICOMATIONT	SINE WAVE	White	Х	0.30	0.32	0.34	
		vville	Y	0.34	0.36	0.38	

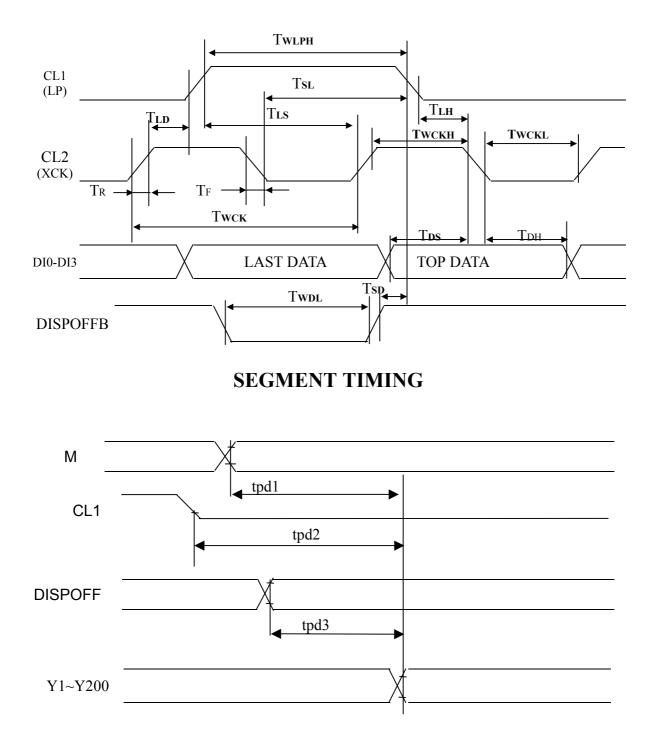
Note: Measured at the EL panel unit .

# 7. TIMING CHARACTERISTICS

## Segment timing:

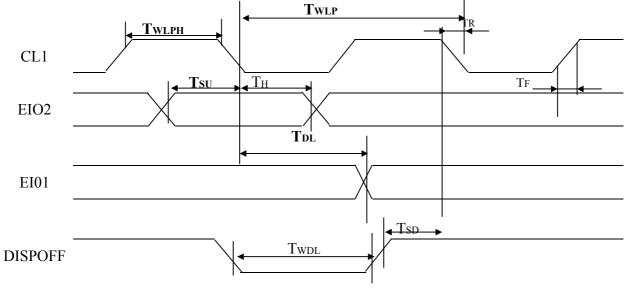
Item	Symbol	Condition	Min.	Тур.	Max.	Units
Shift Clock Period *1	Тwск	Tr,T <b>f≤10ns</b>	125			ns
Shift Clock Pulse Width	Twckh Twckl		51			ns
Data Set Up Time	TDS		30			ns
Data Hold Time	Трн		40			ns
Latch Pulse "H" Pulse Width	Twlph		51			ns
Shift Clock Rise to Latch Pulse Rise Time	Tld		0			ns
Shift Clock Fall to Latch Pulse Fall Time	Tsl		51			ns
Latch Pulse Rise to Shift Clock Rise Time	TLS		51			ns
Latch Pulse Fall to Shift Clock Fall Time	Тін		51			ns
Input Signal Rise/ Fall Time *2	Tr, Tf				50	ns
Enable Setup Time	Ts		36			ns
DISPOFFB Removal Time	Tsd		100			ns
DISPOFFB "L" Pulse Width	Twdl		1.2			u
Output Delay Time (1)	Тр	CL=15pF			78	ns
Output Delay Time (2)	Tpd1, Tpd2	Cl=15pF			1.2	us
Output Delay Time (3)	Трдз	CL=15pF			1.2	us

Note: \*1. Take the cascade connection into consideration. \*2. (Тwcк-TwcкL)/2 is maximum in the case of high speed operation.



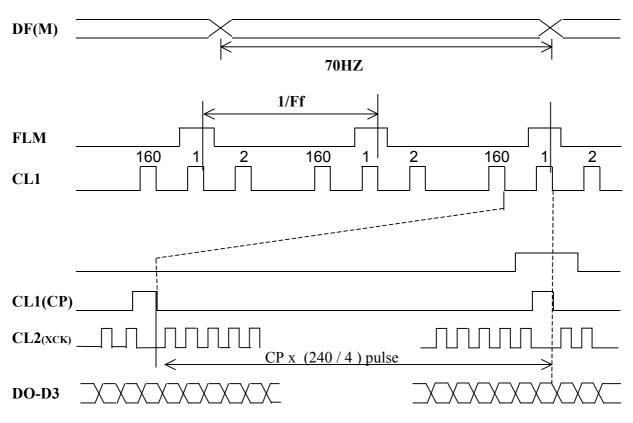
# common timing:

Item	Symbol	Condition	Min.	Тур.	Max.	Units
Shift Clock Period	TWLP	Tr,Tr≤20ns	250			ns
Dulas "11" Dulas Width	Twlph	VDD=+5.0V±10%	15			ns
Pulse "H" Pulse Width	I WLPH	<b>VDD=+2.5V</b> ~+4.5V	30			
Data Set Up Time	Tsu		30			ns
Data Hold Time	Тн		50			ns
Input Signal Rise/ Fall Time	Tr, Tf				50	ns
DISPOFFB Removal Time	Tsd		100			ns
DISPOFFB "L" Pulse Width	Twdl		1.2			u
Output Delay Time (1)	Tdl	CL=15pF			200	ns
Output Delay Time (2)	Тр <b>d1,</b> Тр <b>d2</b>	CL=15pF			1.2	us
Output Delay Time (3)	Tpd3	CL=15pF			1.2	us



**COMMON TIMING** 

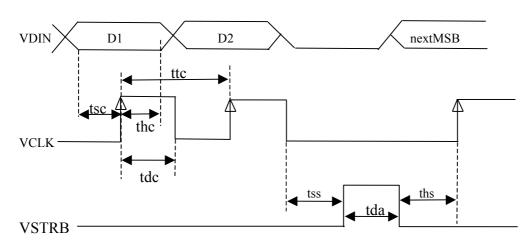
# 7.1 TIMING CHART



### 7.2 S-8330 Timing

## common timing:

Item	Symbol	Condition	Min.	Тур.	Max.	Units
Output Voltage range	VOUT	S-8330A24	12	-	24	V
Potential division accuracy			-	-	±2.4	%
Electric volume resolution			-	1/256	-	
Error in linearity			-	-	$\pm 1/2$	LSB
Data setup time	tsc		0.5	-	-	μs
Data hold time	thc		0.5	-	-	μs
VCLK falling edge to VSTRB rising edge	tss		0.5	-	-	μs
VSTRB pulse width	tda		0.5	-	-	μs
VSTRB falling edge to VCLK rising edge	ths		0.5	-	-	μs
VCLK pulse width	tdc		0.5	4.17	-	μs
VCLK period	ttc		5	8.33	-	μs
VDIN,VCLK,VSTRB input	VSHC1	$V_{IN} \ge 3.0V$	2.4	-	-	v
voltage "H" level	VSHC2	$V_{IN} \leq 3.0V$	1.6	-	-	v
VDIN,VCLK,VSTRB input voltage "L" level	VSLC		-	-	0.2	
VDIN,VCLK,VSTRB input leakage current	ISLC	V <sub>IN</sub> =9V	-1	-	1	μΑ



#### Data input Timing

Note 1: An electric volume has 8-bit resolution. However, its error in linearity may exceed  $\pm 1/2$ LSB only before and after switching from 127 to 128(from 01111111 to 10000000) and vice versa. When the electric volume is used within the variable range of 12 V, its error in linearity corresponds to 6 bits ( $\pm 2.4\%$  potential division accuracy is ensured).

Confidential Document Electric Volume

The S-8330/31 series incorporates an electric volume with an 8-bit shift register and an 8-bit latch. The output voltage is variable in the width of 12V. The output voltage can be held in the data transmission mode because the data read in the shift register is fetched into the latch by unshyncronized strobe input . Figure 1 shows a block diagram and timing charts

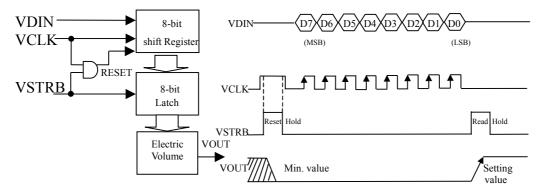
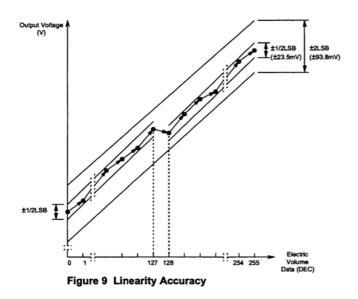


Figure 1 Block Diagram and Timing

VDIN :Inputs data to the electric volume. "Data 1" is input at "H" level; "Data 0" is input at "LOW" level.

- VCLK :Inputs clock to the electric volume. Fetches data at VDIN pin into the shift register at the rising edge of clocks. When clocks of over 8 bits are input, the read data is shifted in succession for each clock, and data corresponding to 8 bits which was input lastly is valid.
- VSTRB :Inputs strobe signal . The contents of the shift register are latched by turning the strobe signal to "H". When the data fetched into the latch is directly transmitted to the electric volume, the output voltage changes. Data in the latch is held by turning the strobe signal to "L".



The electric volume has 8-bit resolution. An error in linearity may exceed  $\pm 1/2$ LSB ( $\pm 23.5 \text{ mV}$ ) only before or after data changeover of  $127 \leftarrow \rightarrow 128$  (0111111( $\leftarrow \rightarrow 10000000$ ) (see Figure 9).

The error in linearity corresponds to 6 bits ( $\pm$ 93.8 mV) when used in the 12 V variable range.

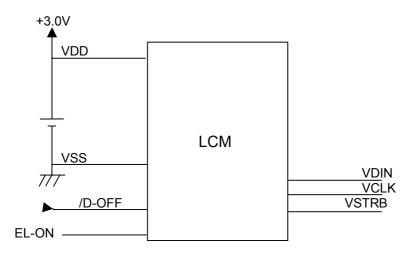
In an application where the variable range of output voltage is 6 V or less, it is possible to use it with high linearity accuracy with appropriate product selection. For example, if you want to change output voltage within the range from 12 V to 18 V, select the S-8330A24FS and use it within the range of  $0 \leftarrow \rightarrow 127$  (0000000 $\leftarrow \rightarrow 01111111$ ). If so, high linearity accuracy (within±1/2LSB) can be obtained.

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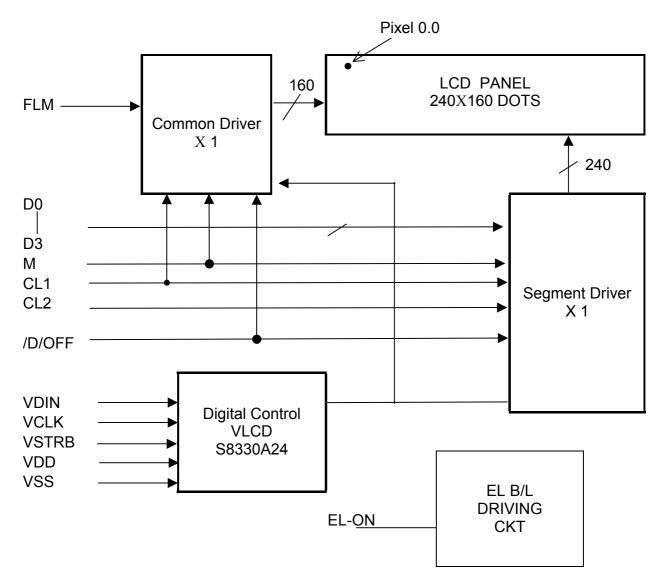
# 8. PIN CONNECTIONS

No.	Symbol	Function	
1	VDIN	S8330 serial data input.	
2	VCLK	S8330 serial clock input.	
3	VSTRB	S8330 strobe signal input.	
4	VDD	Logic Power Input.	
5	FLM	First line mark.	
6	VSS	Ground, 0V	
7	CL1	Data latch pulse.	
8	VSS	Ground, 0V	
9	М	Switch signal to convert LCD drive wave form into AC	
10	/D-OFF	Display ON/ OFF control input ("H"=ON, "L"=OFF)	
11	CL2	Data shift pulse.	
12	NC	NO Connection.	
13	NC	NO Connection.	
14	D3	Input data signal 3.	
15	D2	Input data signal 2.	
16	D1	Input data signal 1.	
17	D0	Input data signal 0.	
18	EL-ON	EL Back light ON/OFF ("H"= OFF, "L"= ON)	

# 9. POWER SUPPLY



## **10.** BLOCK DIAGRAM



### 11. QUALITY ASSURANCE

#### 11.1 Test Condition

- 11.1.1 Temperature and Humidity(Ambient Temperature)
  - Temperature :  $20 \pm 5^{\circ}C$ 
    - Humidity :  $65 \pm 5\%$

#### 11.1.2 Operation

Unless specified otherwise, test will be conducted with LCM in operation.

#### 11.1.3 Container

Unless specified otherwise, vibration test will be conducted on module only.

11.1.4 Test Frequency Single cycle.

#### 11.1.5 Test Method

No.	Parameter	Conditions	Regulations
1	gh Temperature Operating $70 \pm 2 \degree C$		Note 3
2	Low Temperature Operating	-20 ± 2 °C	Note 3
3	High Temperature Storage	80 ± 2 °C	Note 3
4	Low Temperature Storage	v Temperature Storage -30 ± 2 °C	
5	Vibration Test (Non-operation state)	ion state) Total fixed amplitude : 1.5mm Vibration Frequency : 10 ~ 55Hz One cycle 60 seconds to 3 directions of X.Y.Z. for each 15 minutes	
6	Damp Proof Test (Non-operation state)	40°C ± 2°C, 90~95%RH, 96h	
7	Shock Test (Non-operation state)	To be measured after dropping from 60cm high once concrete surface in packing state	Note 3

Note 1: Returned under normal temperature and humidity for 4 hrs.

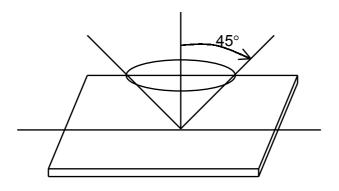
Note 2: No dew condensation to be observed.

Note 3: No change on display and in operation under the test condition

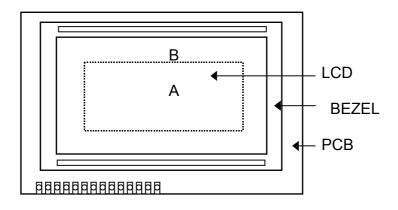
### **11.2 Inspection condition**

11.2.1 Inspection conditions

The LCD shall be inspected under 40W white fluorescent light.



11.2.2 Definition of applicable Zones

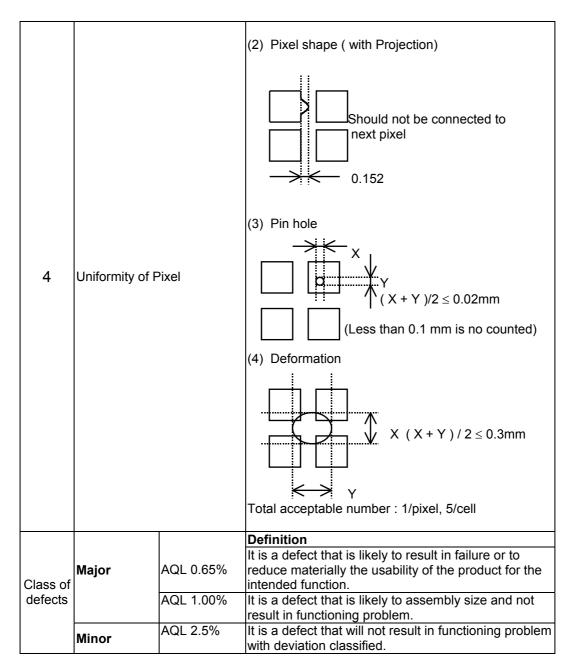


A : Display Area

B : Non-Display Area

#### 11.2.3 Inspection Parameters

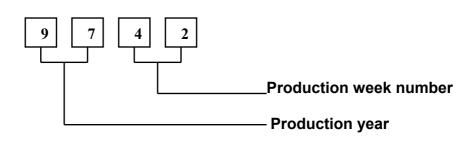
No.	Parameter	Criteria			
1	Black or White spots	ZoneAcceptable numberClass Of Of DefectsAQL LevelDimensionABDefectsD < 0.15**			
		$\begin{array}{ c c c c c c c }\hline 0.15 \le D < 0.2 & 4 & 4 \\ \hline 0.2 \le D \le 0.25 & 2 & 2 \\ \hline D \le 0.3 & 0 & 1 \\ \hline D = (Long + Short) / 2 & * : Disregard \end{array} \qquad $			
2	Scratch, Substances	$\begin{array}{ c c c c c c c c } \hline X \ (nm) \ Y(mm) & A & B & Defects \\\hline \hline $ & 0.04 \ge W & $*$ & $*$ \\\hline \hline $ & 3.0 \ge L & 0.06 \ge W & 4 & 4 & \\\hline $ & 2.0 \ge L & 0.08 \ge W & 2 & 3 & Minor & 2.5 \\\hline \hline \end{array}$			
3	Air Bubbles (between glass & polarizer)	Image: Construction Image: Construction   Imag			
		$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$			
4	Uniformity of Pixel	(1) Pixel shape (with Dent) 0.152			



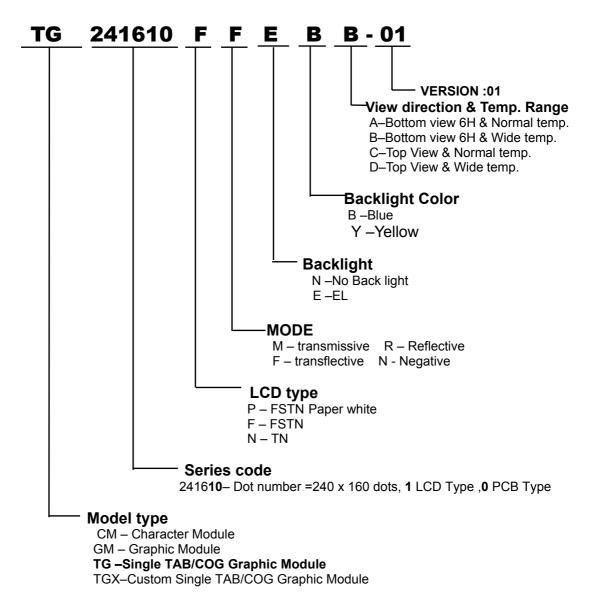
### 11.3 Sampling Condition

Unless otherwise agree in written, the sampling inspection shall be applied to the incoming inspection of customer. Lot size: Quantity of shipment lot per model. Sampling type: normal inspection, single sampling Inspection level: Level II Sampling table: MIL-STD-105E

## 12. LOT NUMBERING SYSTEM



#### 13. LCM NUMBERING SYSTEM



## **14. PRECAUTION FOR USING LCM**

#### 1. LIQUID CRYSTAL DISPLAY (LCD)

LCD is made up of glass, organic sealant, organic fluid, and polymer based polarizers. The following precautions should be taken when handing,

(1). Keep the temperature within range of use and storage. Excessive temperature and humidity could cause polarization degredation, polarizer peel off or bubble.

(2). Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin.

(3). Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.

(4). Glass can be easily chipped or cracked from rough handling, especially at corners and edges.

(5). Do not drive LCD with DC voltage.

2. Liquid Crystal Display Modules

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted. (1). Do not tamper in any way with the tabs on the metal frame.

(2). Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.(3). Do not touch the elastomer connector, especially insert

an backlight panel (for example, EL).

(4). When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting . Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

(5). Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

#### 2.2. Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

(1). The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.

(2). The modules should be kept in antistatic bags or other containers resistant to static for storage.

(3). Only properly grounded soldering irons should be used.(4). If an electric screwdriver is used, it should be well grounded and shielded from commutator sparks.

(5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.(6). Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

2.3 Soldering

(1). Solder only to the I/O terminals.

(2). Use only soldering irons with proper grounding and no leakage.

(3). Soldering temperature :  $280^{\circ}C \pm 10^{\circ}C$ 

(4). Soldering time: 3 to 4 sec.

(5). Use eutectic solder with resin flux fill.

(6). If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed after wards.

#### 2.4 Operation

(1). The viewing angle can be adjusted by varying the LCD driving voltage V0.

(2). Driving voltage should be kept within specified range; excess voltage shortens display life.

(3). Response time increases with decrease in temperature.

(4). Display may turn black or dark blue at temperatures above its operational range; this is (however not pressing on the viewing area) may cause the segments to appear "fractured".

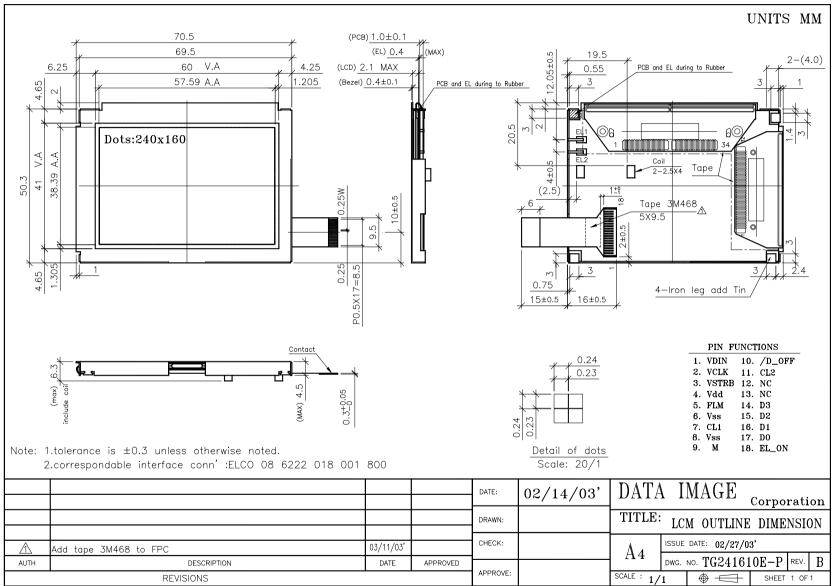
(5). Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured".

#### 2.5 Storage

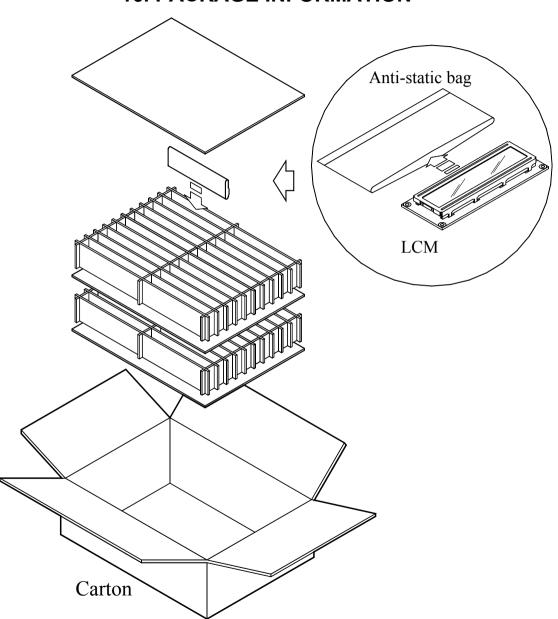
If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

#### 2.6 Limited Warranty

Unless otherwise agreed between DATA IMAGE and customer, DATA IMAGE will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with DATA IMAGE acceptance standards, for a period on one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of DATA IMAGE is limited to repair and/or replacement on the terms set forth above. DATA IMAGE will not responsible for any subsequent or consequential events.



15. OUTLINE DRAWING



# **16. PACKAGE INFORMATION**